

Phase Matching Denoising Algorithm and IP Design

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To cite this article:

Wu Peng, Xia Hui, Wang Chen. Phase Matching Denoising Algorithm and IP Design. *International Journal of Intelligent Information Systems*. Vol. 5, No. 5, 2016, pp. 71-74. doi: 10.11648/j.ijis.20160505.13

Received: September 18, 2016; Accepted: October 10, 2016; Published: October 17, 2016

Abstract: At present, there are many kinds of denoising algorithms, but the hardware of these algorithms IP design is very lacked. In this paper, a phase matching denoising algorithm is proposed to remove the noise, and the IP design of this method is realized. Phase matching denoising algorithm is based on the signal phase matching method to obtain the actual signal and remove noise. Mathematical expression and mathematical proof of the method are given here. Using this method, the IP kernel design is given and the design method is given. Through the function simulation, it is proved that the IP kernel design can effectively remove the noise in the signal.

Keywords: Phase Match, IP Kernel, De-noise, Simulation

1. Introduction

With the continuous development of science and technology, the current electronic system is a powerful and complex structure. Because of the complex system structure, the system development is generally divided into several levels [1]. Each level is composed of a number of modules. In the actual development process, the soft IP and hard IP can be used as the development process components. They can be placed in a resource pool when the system needs to be called. Signal denoising IP kernel is very lacked [2]. In addition, design method based on an integrated platform is a kind of stable IP framework design method. Based on these reasons [3], Quartus Prime design platform is used to design IP kernel of the phase matching denoising algorithm.

2. Phase Matching Denoising Algorithm

Phase matching denosing algorithm uses two sensor line array to receive signal. The received signal is represented by a formula $r(t) = s(t) + n(t)$, the received signal is the original signal and noise signal linear additivity. Figure 1 shows a linear array of two elements, the array element 1 and the array element 2 simultaneously receive the signal [4].

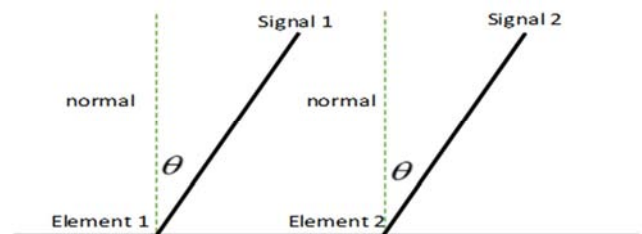


Fig. 1. Signal receiving array.

In Figure 1 the array element 1 receives signal 1 and element 2 receives signal 2. Mathematical equations is expressed as the following form:

$$\begin{aligned} r_1(t) &= s_1(t) + n(t) \\ r_2(t) &= s_2(t) + n(t) \end{aligned} \quad (1)$$

In equations (1) $r_1(t)$ and $r_2(t)$ are the signal received by the array element. They are known signal. $s_1(t)$ and $s_2(t)$ are the original signal which need to restore and desired signal. $n_1(t)$ and $n_2(t)$ are isotropic random noise.

In the noise environment, the distance between array element 1 and the array element 2 is close and the signal acquisition is at the same time [5, 6]. The received noise is close to the same noise because of the isotropic noise. the equations (1) can be written in the following form:

$$\begin{aligned} r_1(t) &= s_1(t) + n(t) \\ r_2(t) &= s_2(t) + n(t) \end{aligned} \quad (2)$$

Fourier transform of the two sides of the equations (2) can be obtained as follows:

$$\begin{aligned} R_1(\omega) &= S_1(\omega) + N(\omega) \\ R_2(\omega) &= S_2(\omega) + N(\omega) \end{aligned} \quad (3)$$

In order to study the problem, the equations (3) is expressed as follows in the frequency domain polar coordinates:

$$\begin{aligned} |R_1|e^{j\phi_1} &= |S_1|e^{j\alpha_1} + |N|e^{j\beta} \\ |R_2|e^{j\phi_2} &= |S_2|e^{j\alpha_2} + |N|e^{j\beta} \end{aligned} \quad (4)$$

In equations (4), ϕ_1 , ϕ_2 , α_1 , α_2 and β are respectively the phase angle of the received signal, the the desired signal and the noise signal. These phase angles are the functions of ω . $|R|$, $|S|$ and $|N|$ are respectively the spectrum amplitude of the received signal, the desired signal and the noise signal. Phase angle is the antitangent value of ratio. The ratio is the ratio of the real part and the imaginary part of the Fourier transform.

The distance between the array element 1 and the element 2 is set to d . The included angle between the original signal and the normal of the two element array is set to θ . For far field signals, the arrival time of the wave front is not the same.

Time difference is expressed as $\tau = \frac{d}{c} \sin \theta$, c is the wave velocity [7,8]. Because of the arrival time difference, The two original signal phase is different between $s_1(t)$ and $s_2(t)$. Desired signal phase is aligned in the complex domain. Desired signal phase alignment is expressed as follows:

$$|S_2|e^{j\alpha_2}e^{j\omega\tau} = |S_1|e^{j\alpha_1} \quad (5)$$

Take (5) to (4), gets the following equations:

$$\begin{aligned} |R_1|e^{j\phi_1} &= |S_2|e^{j\alpha_2}e^{j\omega\tau} + |N|e^{j\beta} \\ |R_2|e^{j\phi_2} &= |S_2|e^{j\alpha_2} + |N|e^{j\beta} \end{aligned} \quad (6)$$

$s_2(t)$ is obtained by the equation group (6),

$$|R_1|e^{j\phi_1} - |R_2|e^{j\phi_2} = |S_2|e^{j\alpha_2}(e^{j\omega\tau} - 1) \quad (7)$$

The equation (7) plural frequency domain transform is expressed as follows:

$$\begin{aligned} [re(R_1) - re(R_2)] + i[im(R_1) - im(R_2)] \\ = [re(S_2) + i(im(S_2))][(\cos \omega\tau - 1) + i(\sin \omega\tau)] \end{aligned}$$

$re(*)$ is the real part of the plural, $im(*)$ is the imaginary part of the plural. ω is the angular frequency of the signal. If two complex numbers are equal, the real and imaginary parts of the two complex numbers are equal. The following equations are obtained:

$$re(R_1) - re(R_2) = re(S_2)(\cos \omega\tau - 1) - im(S_2)\sin \omega\tau$$

$$im(R_1) - im(R_2) = im(S_2)(\cos \omega\tau - 1) + re(S_2)\sin \omega\tau$$

Equations solution is obtained as follows:

$$re(S_2) = \frac{[re(R_1) - re(R_2)](\cos \omega\tau - 1) + [im(R_1) - im(R_2)]\sin \omega\tau}{2 - 2\cos \omega\tau}$$

$$im(S_2) = \frac{[re(R_1) - re(R_2)]\sin \omega\tau - [im(R_1) - im(R_2)](\cos \omega\tau - 1)}{2\cos \omega\tau - 2}$$

The real part of the desired signal is calculated by the real part and imaginary part of received signals by Fourier transform. The imaginary part of the desired signal can also be calculated. After the desired signal is calculated in the frequency domain, it can estimate the expected time domain waveform of the original signal by the inverse Fourier transform. The phase matching method can resist and eliminate the noise [9]. Desired signal can also be calculated. For low signal to noise ratio signal, do not need to know the type of noise.

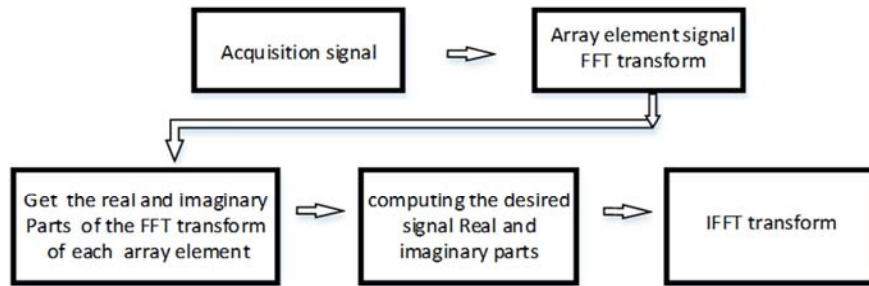


Fig. 2. Phased matching denoising algorithm steps.

Figure 2 shows the phase matching denoising algorithm steps. The first is the acquisition of the signal at the same time in the array. Secondly, the signal is collected by Fourier transform, and the signal phase is aligned. Finally, the real and imaginary parts of the desired signal are calculated by the real

and imaginary parts of the received signal, and then the desired signal is obtained through the inverse Fourier transform.

3. Phase Matching Denoising Algorithm IP Kernel

According to the content of the phase matching denoising algorithm, the IP kernel design is divided into four parts: input, processing, output and control. The input part is the process of the signal sampling, and the sampling data is stored in the memory. Processing part is that the sampling signal in memory is read out and transformed by Fourier transform. By sampling signal real and imaginary parts, the real and imaginary parts of the desired signal are calculated, and the inverse Fourier transform is carried out to restore the desired signal in time domain. The output part is to output the processing result. In fact, the output signal is stored in the output memory. Control part is to coordinate the other parts to work orderly through the control signal. In the structure of the IP kernel design, the part that needs special explanation is the control part. This part is the key of the IP kernel design. This part controls the whole data flow and coordinate each part to work.

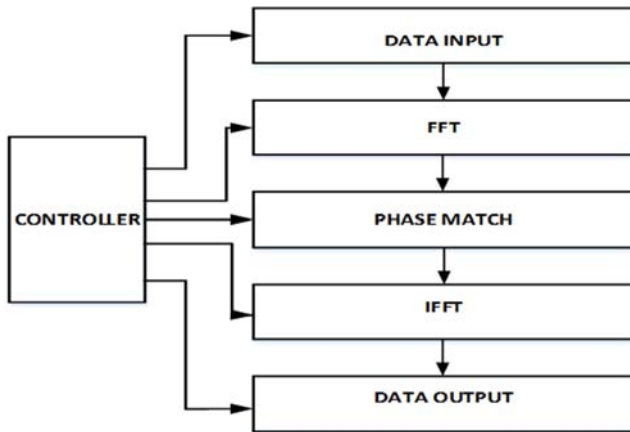


Fig. 3. IP kernel integral structure.

The overall structure of IP kernel is designed as six modules which are control module, data input module, fast Fourier transform module, phase matching module, inverse Fourier transform module and data output module.

The phase matching module will match the data after Fourier transform, the structure of the model is shown in Figure 4.

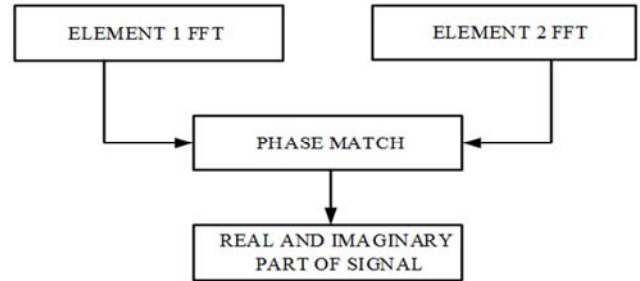


Fig. 4. Integral structure on phased match.

The signal received by the array element 1 and the array element 2 is done by Fourier transform. The transformed results are given to the phase matching part. The matching operation is carried out to obtain the real part and the imaginary part of the desired signal.

4. Simulation

Phase matching denoising algorithm is to receive signals from the sensor. After Fourier transform and phase matching, get rid of noise to get the desired signal. The structure of the IP kernel can be divided into three parts, the Fourier transform part, the phase matching part and the inverse Fourier transform. The several parts can be worked in a pipeline manner. Each part can be run independently and is the output and input relation. The parallel operation can be formed. Control module gives the control signal, so that each part is work orderly[10,11].

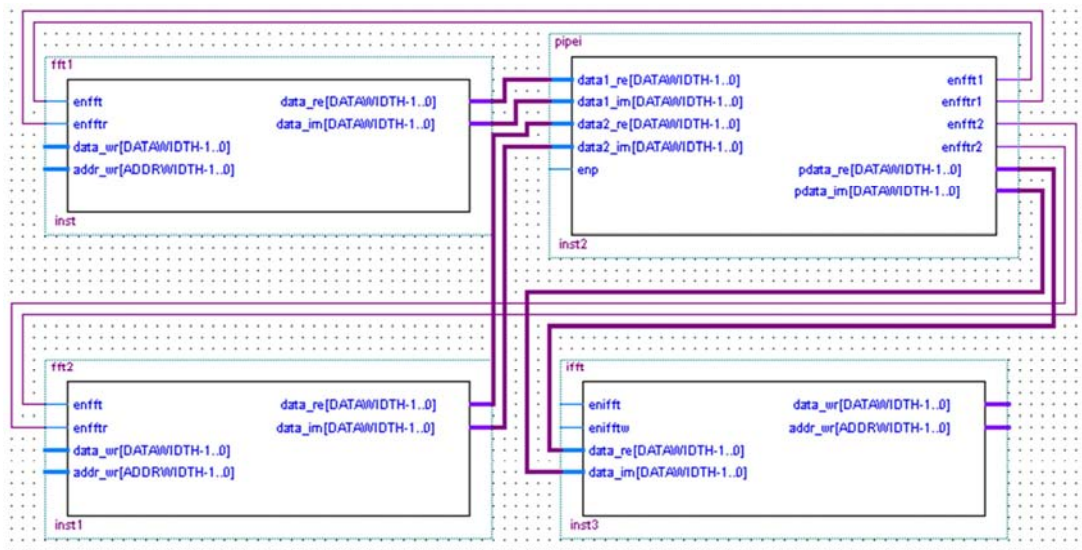


Fig. 5. Double array phase match algorithm schematic.

In Fig. 5 the pipei section receives signals real part and imaginary part from fft1 and fft2. The phase matching operation is carried out under the control signal enp, and the result of the operation is output to the ifft part. enifftw signal control ifft part which is the inverse Fourier transform. Under the control of enifftw signal, time domain data is output.

The original signal is a sine wave of 100Hz frequency. The Gauss white noise is added to the sine wave signal. The signal to noise ratio is -1dB. The number of sampling point is 10K. The IP kernel function simulation of denoising effect is shown in figure6.

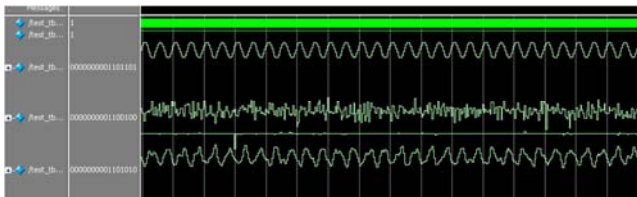


Fig. 6. Phase match denoising algorithm function simulation.

The upper part of the Fig. 6 is the original sine signal. The central part of the figure is the sine signal added Gauss noise. The signal has become chaotic, has not distinguish what is the signal. The lower part of the Fig. 6 is the recovery signal after phase matching. The recovered signal can be seen as a sinusoidal signal.

5. Conclusion

In this paper, the phase matching denoising algorithm and the IP kernel design method are given. The algorithm is analyzed and proved. The IP kernel module design is given. The phase matching part realization of IP kernel is described. Simulation results show that the phase matching denoising algorithm IP kernel design is correct. The IP kernel can remove the Gauss white noise in the low signal to noise ratio, which provides the design idea for the denoising IP kernel design.

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