

Control Power Sharing of Parallel Inverters in Microgrid with Consideration of Line Impedance Effect

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Abstract: This paper presents a power sharing control method for use between paralleled three-phase inverters in an islanded microgrid. In this study, the mismatch of power sharing when the line impedances have significant differences for inverters connected to a microgrid has been solved, the accuracy of power sharing in an islanded microgrid is improved, the voltage droop slope is tuned to compensate for the mismatch in the voltage drops across line impedances by using communication links. The method will ensure in accurate power sharing even if the communication is interrupted. If the load changes while the communication is interrupted, the accuracy of power sharing is reduced but the proposed method is better than the conventional droop control method. In addition, the accuracy of power sharing base on the proposed method is not affected by the time delay in the communication channel and local loads at the output of the inverters. The control model has been simulated in Matlab/Simulink with two or three inverters are connected in parallel. Simulation results demonstrate the accuracy of the proposed control method. Futhermore, in order to validate the theoretical analysis and simulation results, an experimental setup was built in the laboratory. Results obtained from the experimental setup verify the effectiveness of the proposed method.

Keywords: Droop Control, Power Sharing, Microgrid Control, Parallel Inverter, Line Impedance

1. Introduction

In the microgrid, inverters are connected in parallel to form a backup system, improve the reliability, reduce the overload of each inverter, and provide flexibility. However, when a microgrid is operating in the islanded mode, each of the inverter should be able to supply its share of the total load in proportion to its rating. The control strategies for this mode are usually divided into two main types [1-2] as follows. The first type is made up of the communication-based control techniques including concentrated control, master/slave control and distributed control. These techniques can achieve an excellent proper power sharing. However, they required communication lines between the modules which may increase the cost of systems. Long distance communication lines are easy to disrupt, which reduces both system reliability and expandability. The second type is based on the droop control technique without requiring communications, and it is widely used in conventional power systems [2-11]. The reason for the popularity of this droop control technique is that it

provides a decentralized control capability that does not depend on external communication links. These techniques enable the “plug-and-play” interface and enhance the reliability of systems. However, communications can be used in addition to the droop control method to enhance the system performance without reducing reliability [12-22].

Traditional droop control techniques have some disadvantages in terms of power sharing for the following reasons:

1. The line impedances are not available and different from each other. Which has a significant effect on power sharing due to different voltage drops. When the impedances of the lines connecting inverters to the common connection point are different, a current imbalance appears when the load sharing error increases [1].
2. The heterogeneous line impedance, including the resistor and capacitance, is not suitable for conventional droop control with pure resistors or pure capacitance applying for the low voltage distribution [1, 22].

Moreover, with a heterogeneous line impedance, the active and reactive power interact with each other, which leads to difficulty for separate control [1].

3. Although frequency droop techniques can achieve accurate real power sharing, they typically result in poor reactive power sharing due to mismatches in the impedances of the DG unit feeders and the different ratings of the DG units [22-24]. Consequently, the problem of reactive power sharing in islanded microgrids has received considerable attention in the literature and many control techniques have been developed to address this issue [24-27]. Currently, the studies for power sharing between inverters have the following disadvantages:
4. Communication links are used in some droop improvement studies to enhance the accuracy of power sharing, but the implementation of this technique is sensitive to communication delays, delays in delivery can further reduce the accuracy of the power sharing [12-15].
5. The reliability of these studies are also affected when the communication is interrupted [16-22].
6. Improved power sharing methods can reduce the quality of the voltage, such as: virtual output impedance method [22]; droop method combined with signal injects [24].
7. The accuracy of power sharing is enhanced negligible if local loads are connected at the output of each inverter [25-26].
8. The line impedance parameters in these studies are either assumed to be reactance (X is much larger than R), or it is resistor (R is greater than X), but in fact the line parameters are including resistor and reactance, the power sharing results are not practical [27].

Currently, the problem of the reactive power sharing in the islanded microgrids has received a lot of considerable attention in the literatures and many control techniques have been developed to address this issue [28-30], where a mixed H2/H ∞ - based a voltage control loop, and a sliding-mode-control (SMC) - based a current loop, is used as a replacement for the conventional proportional-plus-integral-based cascaded control. This controller can improve the sustainability of the control system if the microgrid has nonlinear loads, unbalanced loads. However, the mathematical model for the SMC controllers is relatively complex, especially when there are local loads. Research [31] presented a method to reduce the burden on the calculation.

The focus of this paper is a proposed method for controlling parallel connected inverters in an islanded microgrid to allow for power sharing according to the ratio of the rated power of the inverters, under the following conditions:

1. There are significant differences in the line parameters from the inverters to the point of common coupling (PCC).
2. The microgrid has the local loads connected at the output of the inverters.
3. The communication is interrupted or delay.

The DSOGI-PLL (Double Second Order Generalized Integrator - Phase Locked Loop) with a generalized integral block and phase lock loop to monitor the exact voltage magnitude and frequency phase at the PCC and support the information for proposed controller. Therefore, the proposed control method can be an alternative for load sharing control in islanded microgrids.

2. Islanded Microgrid Control

2.1. Structure of Islanded Microgrid

The structure of an islanded microgrid is made up of many inverters connected in parallel. A block diagram of inverters is provided, where each inverter is connected to a common bus at the PCC through the line impedance, is shown in Figure 1. In addition, the loads of a microgrid are also connected to the common bus. The proposed controller contains two control loops, where the outer loop power control divides the capacity of each inverter and the inner loop control makes the voltage and current output of the inverters similar to the references. The voltage magnitude signal from the PCC are provided by a low-bandwidth connection. The inner loops are the current and voltage control to adjust the current and voltage at the inverter output.

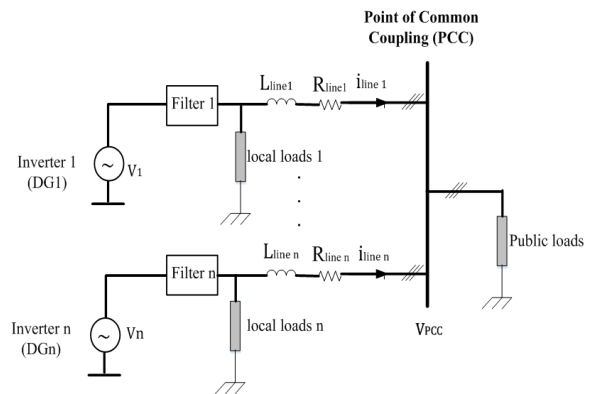


Figure 1. Block diagram of inverters in an islanded microgrid.

2.2. The Principle of the Droop Control Method

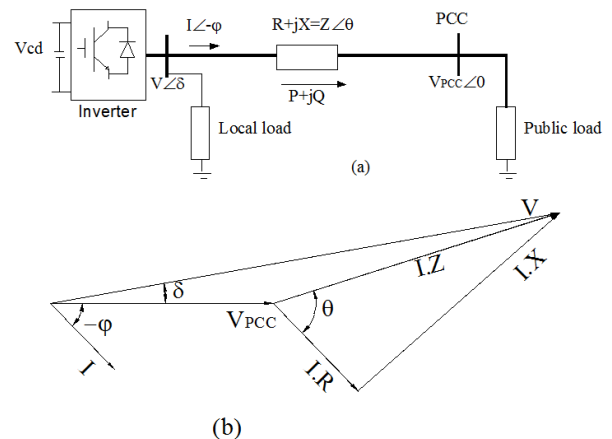


Figure 2. Diagrams showing: (a) equivalent schematic of an inverter connected to loads; (b) vector diagram of voltage and current.

The proposed controller consists of the following main blocks:

3.1. The Proposed Reactive Power Sharing Controller

In this paper, the voltage droop slope is tuned to compensate for the mismatch in the voltage drops across line impedances by:

$$V_{ref} = k_p \int (V' - V_{PCC}) dt \quad (11)$$

Where: V' is the voltage at the output of the traditional Droop controller, which is determined by the Eq. (6).

$$V' = V_0 - m_q Q' \quad (12)$$

Where: k_p is the gain of the integral, V_{pcc} is the voltage at PCC.

3.2. The Proposed Power Sharing Controller

The proposed controller to active power sharing and reactive power sharing are shown in Figure 4.

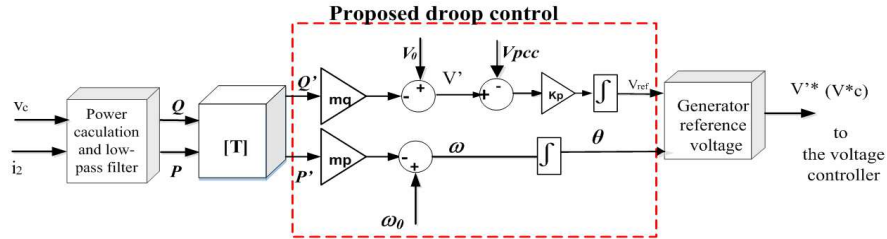


Figure 4. Proposed active power sharing and reactive power sharing control.

3.3. Survey the Stability of the Control System

From (1), (2), (3) and (4), we can write:

$$P' = \frac{VV_{PCC} \sin(\delta - \delta_{PCC})}{Z} \quad (13)$$

$$Q' = \frac{V^2 - VV_{PCC} \cos(\delta - \delta_{PCC})}{Z} \quad (14)$$

where V_{pcc} are the output of DSOGI-PLL blocks, V is the output of the reactive power sharing from the controller, and δ is the output of the active power sharing controller.

By linearizing (11), (12) and (14) around Q' , V and V_{pcc} the following is obtained:

$$\Delta V_{ref} = k_p \int (\Delta V' - \Delta V_{PCC}) dt \quad (15)$$

$$\Delta V' = \Delta V_0 - m_q \Delta Q' \quad (16)$$

$$\Delta Q' = \frac{\partial Q'}{\partial V} \Delta V + \frac{\partial Q'}{\partial V_{PCC}} \Delta V_{PCC} = A \Delta V + B \Delta V_{PCC} \quad (17)$$

Where:

$$A = \frac{2V - V_{PCC} \cos(\delta - \delta_{PCC})}{Z}$$

$$B = -\frac{V}{Z} \cos(\delta - \delta_{PCC})$$

The relationships among (15), (16) and (17) are shown in Figure 5.

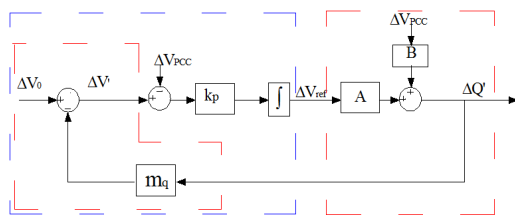


Figure 5. Small signal reactive power sharing droop control.

The transfer function of Figure 5 is as follows:

$$\Delta Q'(S) = \frac{k_p A}{s + k_p m_q A} \Delta V_0(S) + \frac{SB - k_p A}{s + k_p m_q A} \Delta V_{PCC}(S) \quad (18)$$

From (18), λ can be calculated as:

$$\lambda = -k_p m_q A \quad (19)$$

The transfer function (18) has shown that the constant of the loops control can be adjusted by k_p , and not by m_q . The reactive power sharing no longer affects the quality of the voltage or frequency.

By linearizing (5) and (13) around P' , δ and δ_{pcc} the following is obtained:

$$\Delta \omega = \Delta \omega_0 - m_p \Delta P' \quad (20)$$

$$\Delta P' = C \Delta \delta + D \Delta \delta_{PCC} \quad (21)$$

Where:

$$C = \frac{VV_{PCC} \cos(\delta - \delta_{PCC})}{Z}$$

$$D = -\frac{VV_{PCC}}{Z} \cos(\delta - \delta_{PCC})$$

$$\Delta P' = C \Delta \delta + D \Delta \delta_{PCC} = C(\Delta \delta - \Delta \delta_{PCC}) \quad (22)$$

The relationships among (20) and (22) are shown in Figure 6.

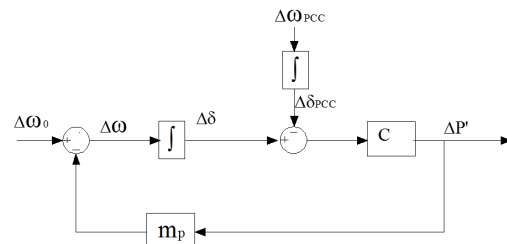


Figure 6. Small signal active power sharing droop control.

The transfer function of Figure 6 is as follows:

$$\Delta P'(S) = \frac{c}{s+m_p \cdot c} \Delta \omega_0(S) + \frac{c}{s+m_p \cdot c} \Delta \omega_{PCC} \quad (23)$$

From (23), λ can be calculated as:

$$\lambda = -m_p \cdot c \quad (24)$$

m_p is determined by the Equation (7).

3.4. The Current and Voltage Controller

The voltage and current controller employs standard proportional-integral (PI) regulators established based on the circuit equations from Equation (25) to Equation (28), which are written in the $dq0$ coordinates of the equivalent circuit as

follows:

$$i_{1d} = i_{2d} + C \frac{dv_{cd}}{dt} - \omega C v_{cq} \quad (25)$$

$$i_{1q} = i_{2q} + C \frac{dv_{cq}}{dt} + \omega C v_{cd} \quad (26)$$

$$v_{inv,d} = L_f \frac{di_{1d}}{dt} + R_f i_{1d} - \omega L_f i_{1q} + v_{cd} \quad (27)$$

$$v_{inv,q} = L_f \frac{di_{1q}}{dt} + R_f i_{1q} + \omega L_f i_{1d} + v_{cq} \quad (28)$$

Where i_{1d} and i_{1q} are the current of inverter; i_{2d} and i_{2q} are the current on the line; and v_{cd} and v_{cq} are the voltage of the capacitor.

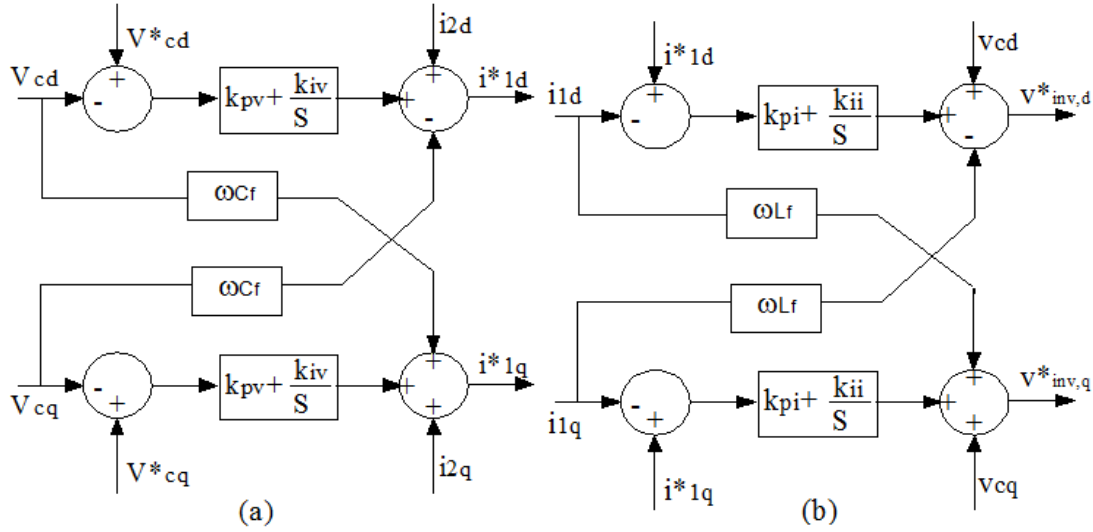


Figure 7. Schematics showing: (a) voltage controller; (b) current controller.

3.5. Modeling of a Three Phase DSOGI-PLL

Figure 8 shows the structure of a DSOGI-PLL. Both of the adaptive filtering technique and the in-quadrature phase detection technique are used in the DSOGI-PLL to generate the frequency and phase outputs. This system has a double

feedback loop, i.e. the frequency/phase generator provides both the phase-angle to the Park transform and the central frequency to the second order-generalized integrator - quadrature signal generation (DSOGI-QSG).

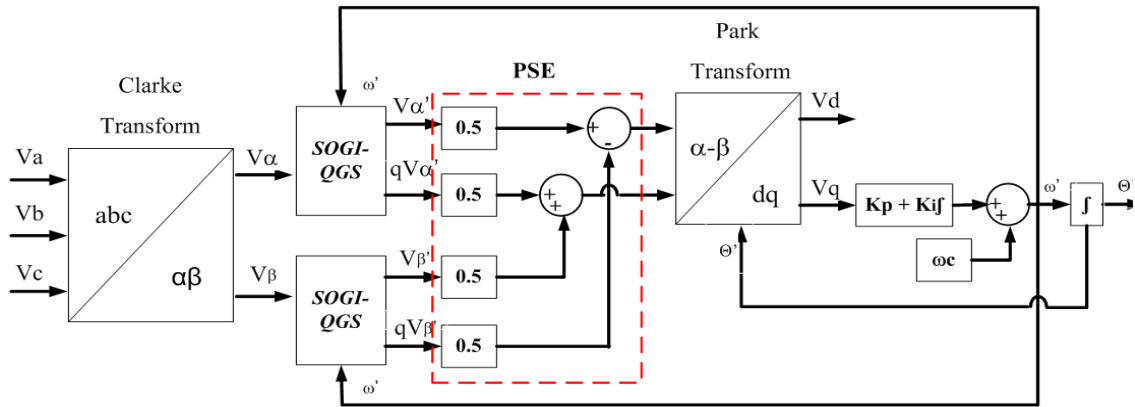


Figure 8. Modelling of a three phase DSOGI-PLL.

The parameters of the DSOGI-PLL are chosen as follows: $k = \sqrt{2}$, $t_s = 100\text{ms}$, $\varepsilon = 1/\sqrt{2}$ and $T_i = t_s^2/2.3 = 0.021\text{s}$.

Figure 9 shows the responses of the DSOGI-PLL.

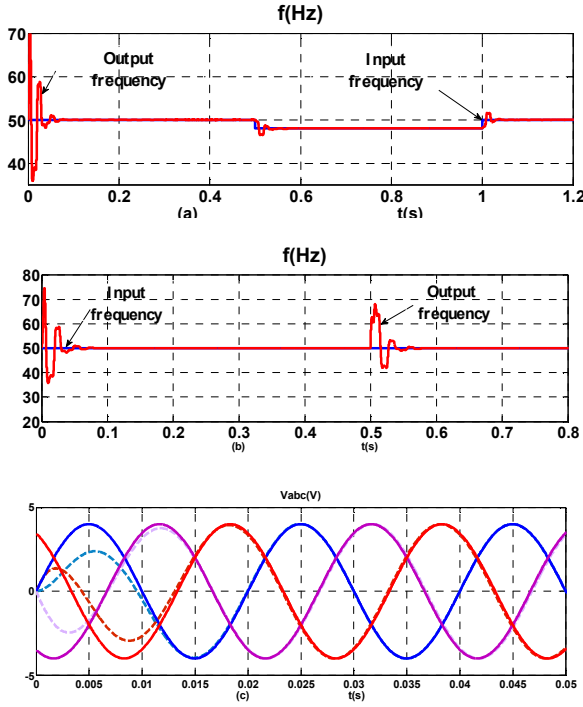


Figure 9. Responses of a DSOGI-PLL.

Figure 9 (a) shows the frequency response of a DSOGI-PLL when the frequency of the input signal changes from 50Hz to 48Hz at $t = 0.5$ s, and from 48Hz to 50Hz at $t = 1$ s. Figure 9 (b) shows the frequency response of a DSOGI-PLL when the phase angle of the input signal changes from 0° to 45° at $t = 0.5$ s. Figure 9 (c) shows the response of the input and output voltages of a DSOGI-PLL. The simulation results in Figure 9 show that the DSOGI-PLL can obtain the exact voltage amplitude and frequency at the point of common coupling (PCC). The voltage amplitude is the input for the inner-controller. Therefore, when more exact values are obtained, more accurate power sharing is achieved.

3.6. Analyze the Effect of Local Loads on Reactive Power Sharing

Active power sharing based on frequency droop is not affected by local loads. However, local loads affecting reactive power sharing during islanding operation [15-28], is shown in Figure 10.

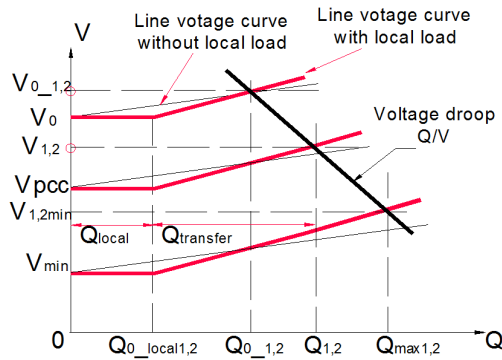


Figure 10. Reactive power flows of two inverters with local loads and line impedances are the same.

A number of things can be seen in Figure 10. When the microgrid does not have local loads, the slope $k_{q1,2}$ is obtained as follows:

$$k_{q1,2} = \frac{V_{0,1,2} - V_0}{Q_{0,1,2}} \quad (29)$$

When the microgrid has local loads, the slope k_q is obtained as follows:

$$k_{q1,2} = \frac{V_{0,1,2} - V_0}{Q_{0,1,2} - Q_{0,local1,2}} \quad (30)$$

Where:

V_0 : the nominal amplitude voltage at the PCC

$V_{0,1,2}$: the nominal amplitude voltage of inverters 1, 2.

$Q_{0,1,2}$: the nominal reactive power of inverters 1, 2.

$Q_{0,local1,2}$: the nominal reactive power of local loads 1, 2.

Different local loads or different inverters leading to reactive power sharing is inaccurate as shown in Figure 11 and Figure 12.

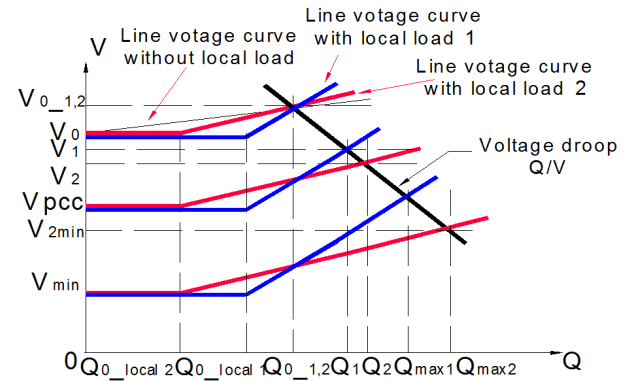


Figure 11. Reactive power flows of two identical inverters and different local loads.

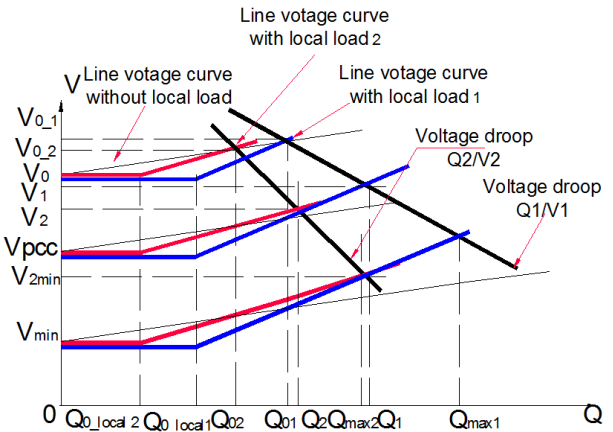


Figure 12. Reactive power flows of two different inverters and different local loads.

According to Figure 11, when the microgrid has local load 1, the slope k_{q1} is obtained as follows:

$$k_{q1} = \frac{V_{0,1,2} - V_0}{Q_{0,1,2} - Q_{0,local1}} \quad (31)$$

According to Figure 11, when the microgrid has local load 2,

the slope k_{q2} is obtained as follows:

$$k_{q2} = \frac{V_{0.12} - V_0}{Q_{0.12} - Q_{0.local2}} \quad (32)$$

According to Figure 12, when the microgrid has local load 1, the slope k_{q1} is obtained as follows:

$$k_{q1} = \frac{V_{0.1} - V_0}{Q_{0.1} - Q_{0.local1}} \quad (33)$$

According to Figure 12, when the microgrid has local load 2, the slope k_{q1} is obtained as follows:

$$k_{q2} = \frac{V_{0.2} - V_0}{Q_{0.2} - Q_{0.local2}} \quad (34)$$

Figure 10, Figure 11 and Figure 12 shown that when microgrid has local loads at the output of the inverters, the local loads will make to changes the output voltage of the

inverters, the voltage of the local loads are equal to the voltage at the PCC. Therefore, the local loads make an offset in the output voltage of the inverters, which is the cause of mismatch for reactive power sharing in islanded microgrid.

By adjusting the integral gain coefficients k_p for the proposed controllers at the Equation (11), when it is in the set state, the voltages V' of inverters will come to an equal voltage ($V'_1 = V'_2 = \dots V'_n = V_{PCC}$). This means that the deviation of the voltage drop across the line and the difference of the local loads are eliminated. In other words, the effect of the deviation of the line impedance and the difference of local loads are eliminated. As a result, if inverters are the same, local loads and line impedance are the same or different, the power sharing for each inverter is:

$$P_1 = P_2 = P_n = \frac{1}{n} (P_{public} + P_{local1} + P_{local2} + \dots + P_{localn}) \quad (35)$$

$$Q_1 = Q_2 = Q_n = \frac{1}{n} (Q_{public} + Q_{local1} + Q_{local2} + \dots + Q_{localn}) \quad (36)$$

3.7. The Improve Proposed Controller

Proposed droop controller in Figure 4 was added to the block composed of logic gates in order to improve reliability for the controller in case of communication is interrupted. The time out/enable logic is shown in Figure 13.

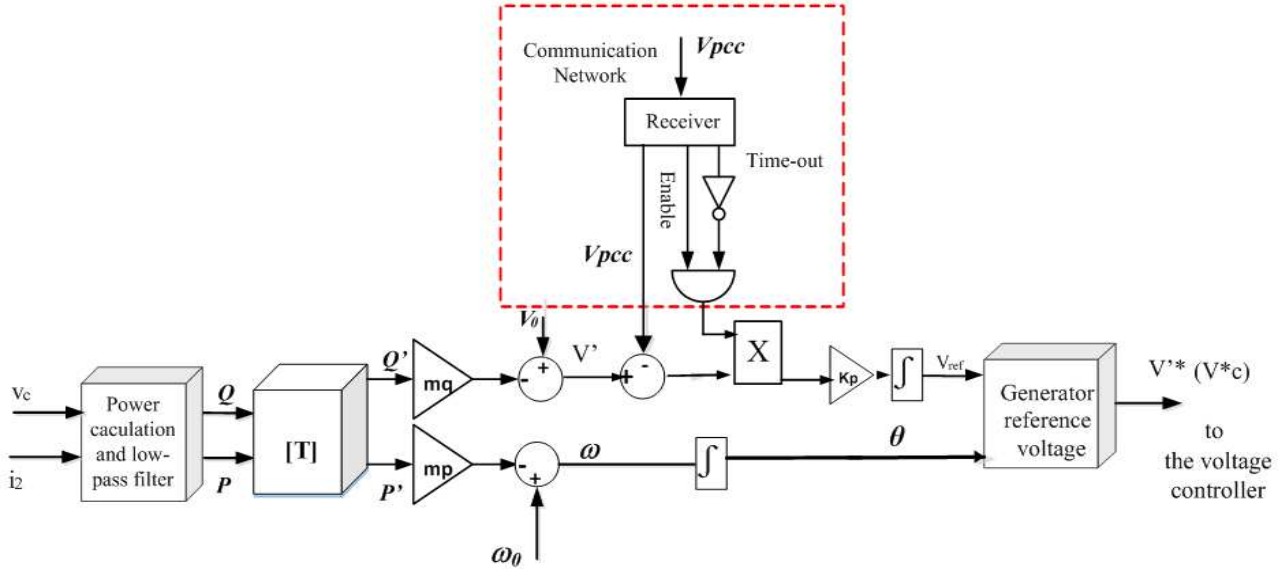


Figure 13. Proposed active power sharing and reactive power sharing control is improved.

When the communication is interrupted, in which case the control loop is disabled and the integrator output will remain constant until the communication is restored. The amplitude voltage at output of proposed droop are held at the last value before the communication failure occurred due to the integral action of the controller. The power sharing is still accurate if the operating point remains unchanged after the communication failure, but if the load changes the power sharing error is still acceptable.

The time delay is called the information update delay. The proposed droop controller is immune to the time delay in the communication channel. Communication link only used to set the value of the reference voltage for tuning the output voltage

of the controller. Moreover, the reference voltage is the amplitude value therefore the system will reach steady state despite is slower than usual. If delays occur in steady state, it will not affect the power sharing accuracy. The reference voltage depends on the load so it is a fixed reference voltage until the load changes. Therefore, the accurate power sharing at steady state is unaffected by time delays in the communication channels.

4. Simulation Results and Discussion

A microgrid with two or three parallel inverters, as shown in

Figure 1, is simulated in Matlab/Simulink. All of the simulation parameters of the system are given in Table 1.

Table 1. Parameters for the controllers.

Parameters	Values	Parameters	Values
Input source voltage V_{cd} (V)	600	Rate frequency f_0 (Hz)	50
Filter inductance L_f (mH)	1.2	Rate power (kVA)	5
Filter resistance R_f (Ω)	0.2	Rate voltage $V_{AC,p}$ (V)	310
Filter capacitance C (μ F)	50	Droop coefficient m_q (V/Var)	1.7e-3
Switching frequency f_0 (kHz)	10	Droop coefficient m_p (rad/s /W)	1e-4

4.1. Simulation for Power Sharing of Two Identical Inverters, the Line Impedances Are Difference

In this case, the line parameters of the two inverters are given in Table 2. The simulation results for this case including the real power output, reactive power output, current output and load voltage are shown in Figures 14 to 17.

Table 2. Line parameters of two inverters.

Line parameters	Inverter 1	Inverter 2
Resistance R (Ω)	0.8	1
Inductance L (mH)	0.6	0.5

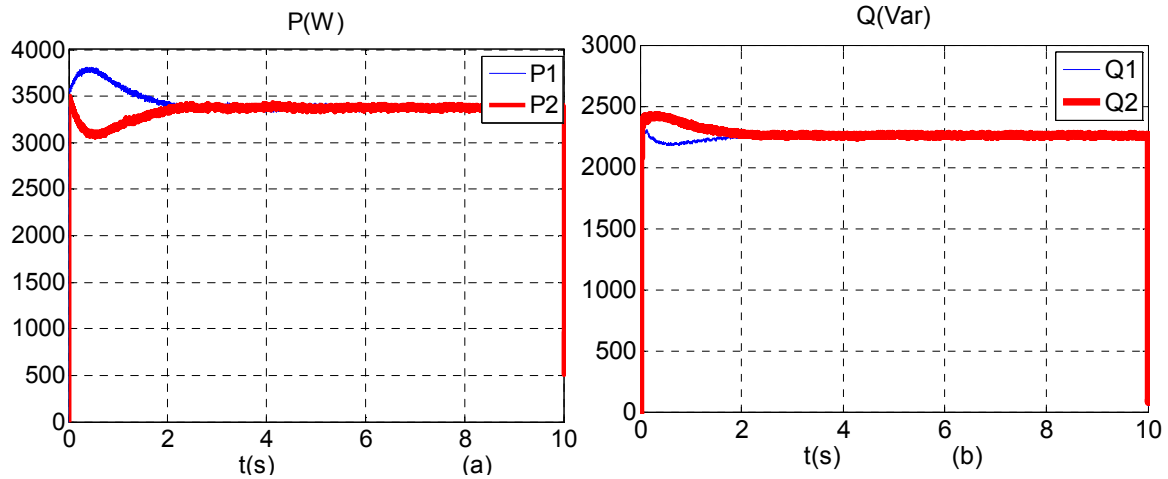
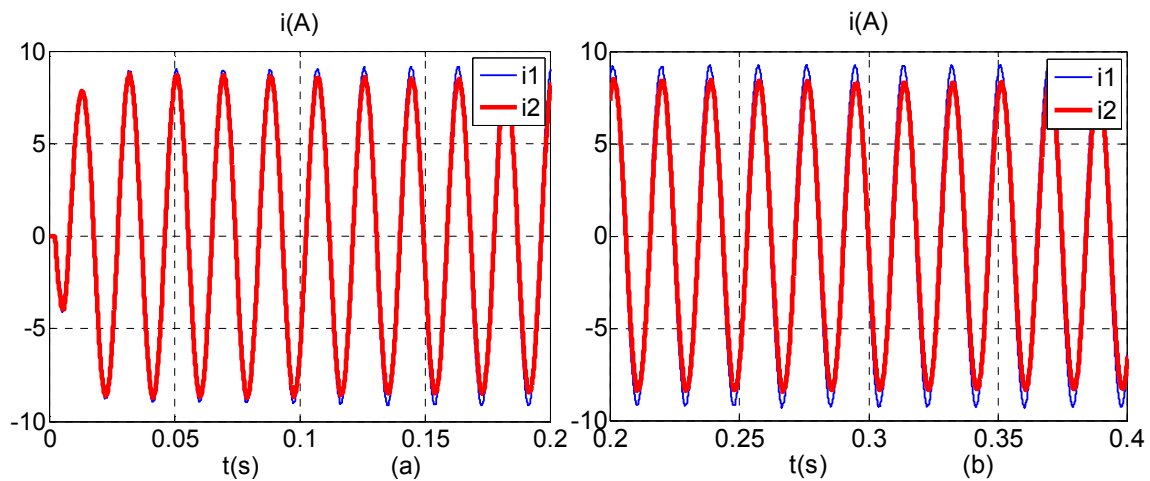


Figure 14. (a) Real power; (b) reactive power.

Figure 14 (a) and Figure 14 (b) show the real and reactive power sharing of each inverter, the power sharing performance is really good with the proposed strategy.

Figure 15 (a), Figure 15 (b) and Figure 15 (c) are shown response of phase current at output of inverter, we can see that during this time the controller has not reached the set state so there is a mismatch in the power sharing, so that the phase current is mismatch also.



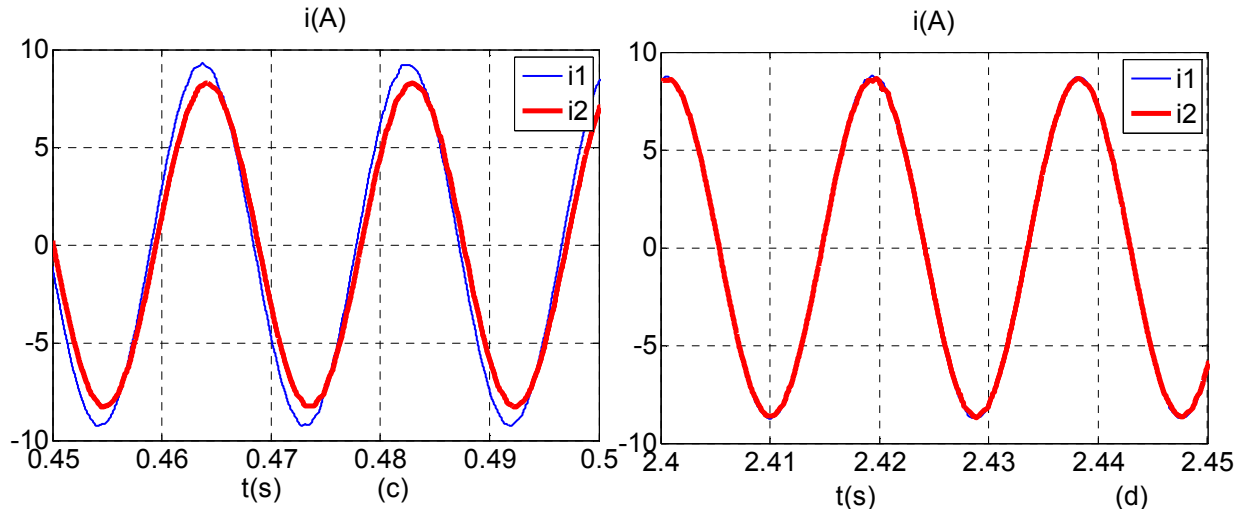


Figure 15. The current output of inverters.

Figure 15 (d) is shown the response of phase current in satability, the current sharing is not mismatched.

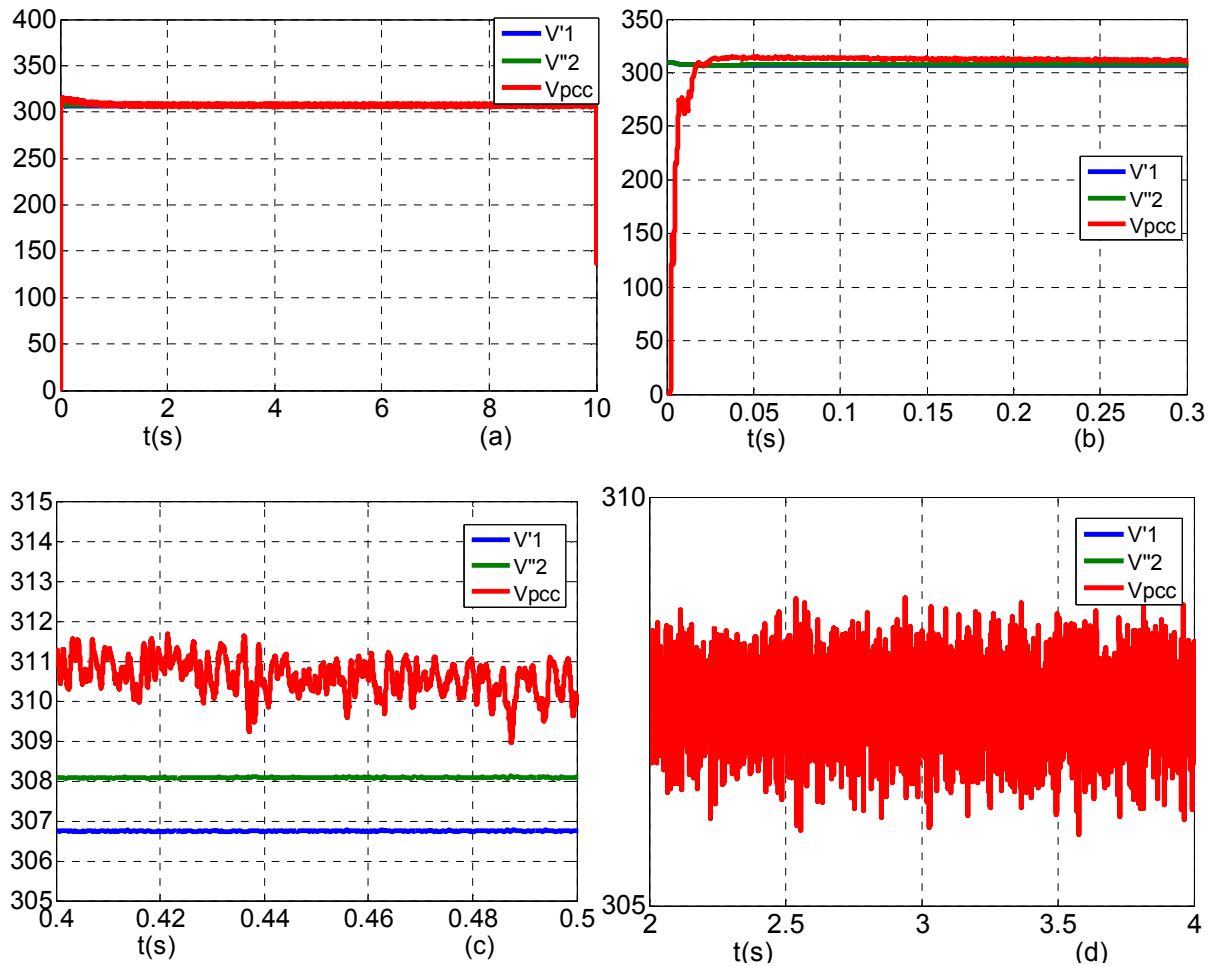


Figure 16. The voltage at PCC and voltage V' of the proposed controller.

Figure 16 is shown the voltage at PCC and voltage V' of the proposed controller, Figure 16 (c) shows that when the proposed controller has not reached the set state, the voltage $V'1$ and the voltage $V'2$ are different, Figure 16 (d) shows that when the proposed controller has reached the set state, the voltages $V'1$, $V'2$, and V_{pcc} are equal, at this time the power sharing is not mismatch.

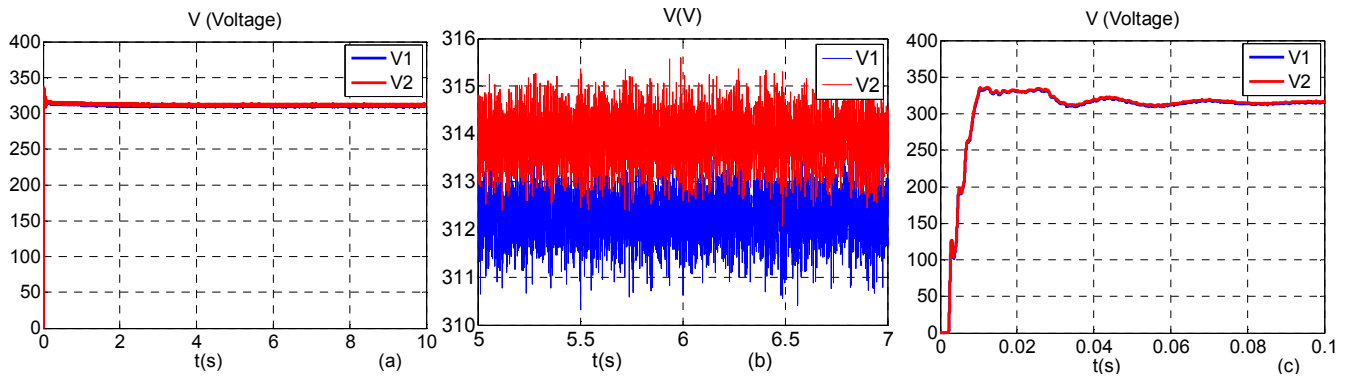


Figure 17. The voltage at the output of inverters.

Figure 17 shows the response of the voltages at the output of inverters, Figure 17 (b) shows the output voltages of the inverters are different, this is due to the mismatch of line impedances.

4.2. Simulation for Power Sharing of Two Identical Inverters, the Line Impedances Are Difference, the Loads Are Changed

In this case, the line parameters of the two inverters are given in Table 3.

Table 3. Line parameters of two inverters.

Line parameters	Inverter 1	Inverter 2
Resistance R (Ω)	0.6	1.0
Inductance L (mH)	0.7	1.0

Parameters of loads:

$t=0-4s$: $P=2300W$, $Q=550Var$, $\cos\phi=0.9$

$t=4-8s$: $P=3400W$, $Q=2250Var$, $\cos\phi=0.83$

$t=8-12s$: $P=1000W$, $Q=900Var$, $\cos\phi=0.74$

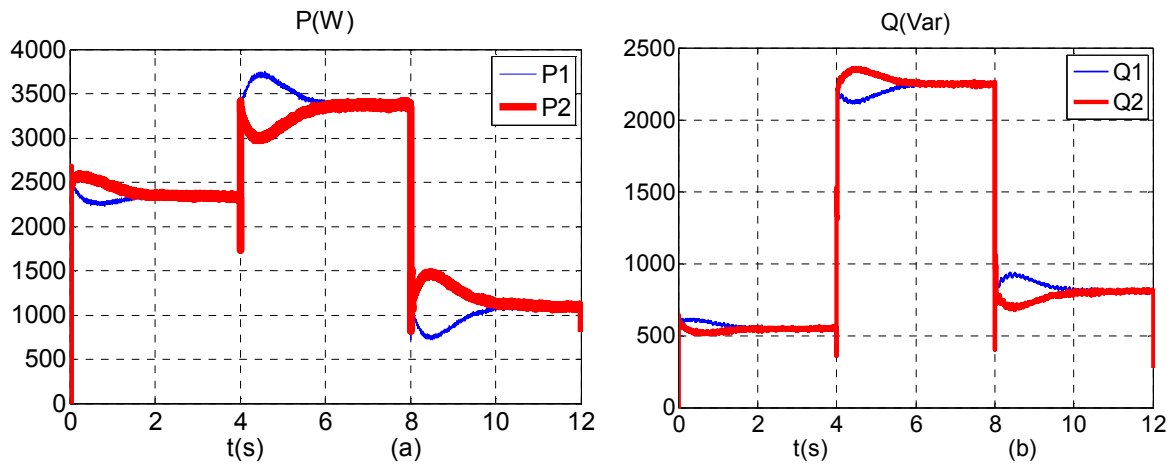


Figure 18. (a) Real power; (b) reactive power.

The deviation of active power and reactive power are divided:

$$e_p \% = \frac{P_i - P_i^*}{P_i^*} \cdot 100\% \quad e_q \% = \frac{Q_i - Q_i^*}{Q_i^*} \cdot 100\%$$

P_i , Q_i are the active power and reactive power measured at the output of the inverter i

P_i^* , Q_i^* are the active power and reactive power desired effect divided for the inverter i .

The deviation of active power in the period from 4s to 8s:

$$e_p \% = \frac{P_i - P_i^*}{P_i^*} \cdot 100\% = \frac{3360 - 3350}{3350} \cdot 100\% = 0,3\%$$

The deviation of reactive power in the period from 4s to 8s:

$$e_q \% = \frac{Q_i - Q_i^*}{Q_i^*} \cdot 100\% = \frac{2255 - 2250}{2250} \cdot 100\% = 0,22\%$$

Figure18 shows that the proposed controller has result in good power sharing when the power of load varies.

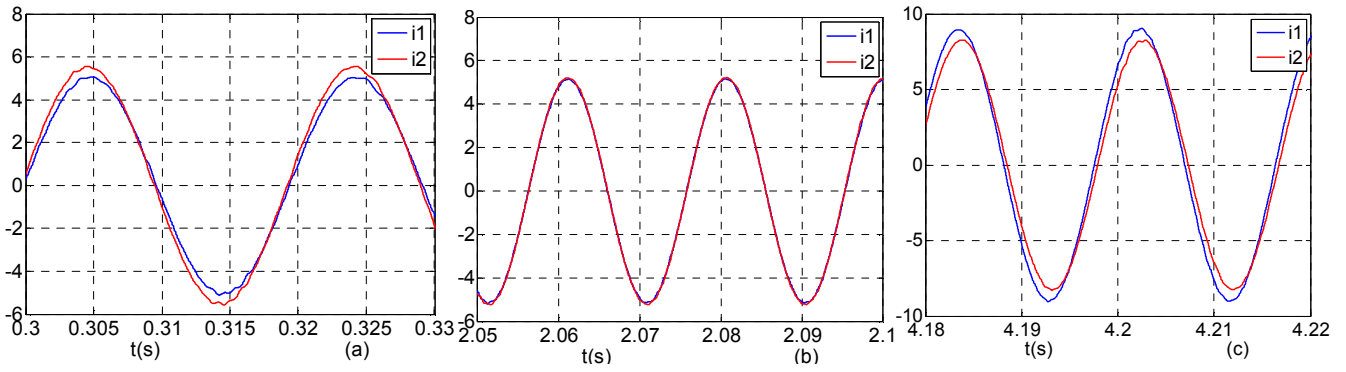


Figure 19. The current output of inverters.

Figure 19 (a) and Figure 19 (c) are shown response of phase current at output of inverter, we can see that during this time the controller has not reached the set state so there is a mismatch in the power sharing, so that the phase current is mismatch also.

Figure 19 (b) is shown the response of phase current in satability, the current sharing is not mismatched.

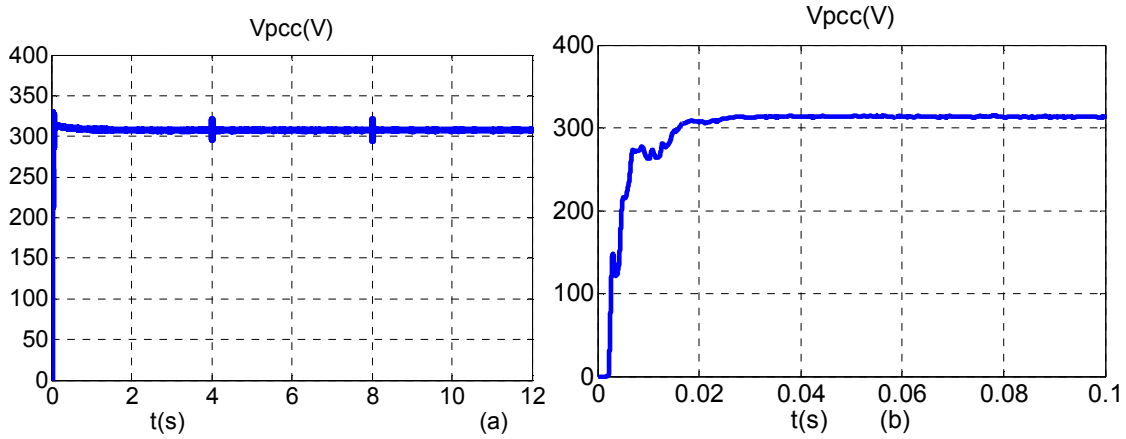


Figure 20. The voltage at PCC.

Figure 20 shows the voltage quality at the PCC, the voltage quality is always guaranteed by proposed controller.

4.3. Simulation for Power Sharing of Two Difference Inverters ($P1: P2=2:1$), the Line Impedances Are Difference

In this case, the line parameters of the two inverters are given in Table 4.

Table 4. Line parameters of two inverters.

Line parameters	Inverter 1	Inverter 2
Resistance R (Ω)	0.4	0.8
Inductance L (mH)	0.6	1.0

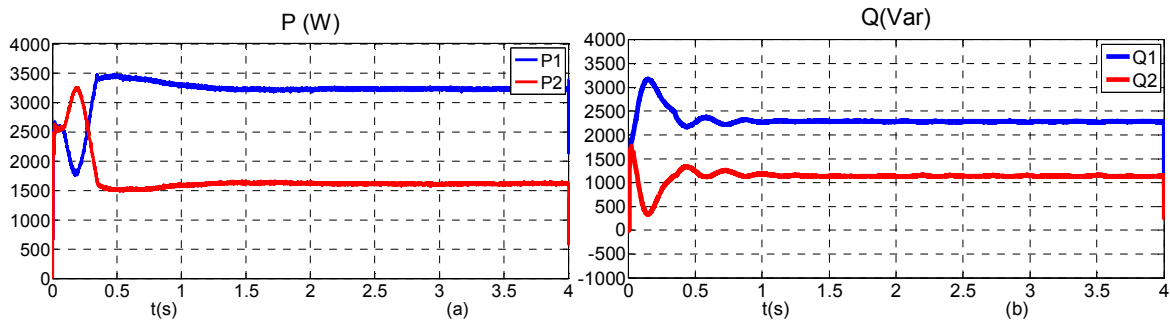


Figure 21. (a) Real power; (b) reactive power. From Figure 21 (a) and Figure 21 (b), it can be seen that the proposed control method provides a good power sharing. Figure 21 can shows accurate real and reactive power with a 2:1 ratio.

Total output power of each inverter:

$$P_1 = \frac{2}{3}(P_{local1} + P_{local2} + P_{public}) = \frac{2}{3}(700 + 760 + 3400) = 3240W$$

$$P_2 = \frac{1}{3}(P_{local1} + P_{local2} + P_{public}) = \frac{1}{3}(700 + 760 + 3400) = 1620W$$

$$Q_1 = \frac{2}{3}(Q_{local1} + Q_{local2} + Q_{public}) = \frac{2}{3}(500 + 700 + 2250) = 2300Var$$

$$Q_2 = \frac{1}{3}(Q_{local1} + Q_{local2} + Q_{public}) = \frac{1}{3}(500 + 700 + 2250) = 1150Var$$

4.4. Simulation for Power Sharing of Three Identical Inverters ($P_1: P_2: P_3=1:1:1$), the Line Impedances Are Difference

In this case, the line parameters of the three inverters are given in Table 5.

Table 5. Line parameters of three inverters.

Line parameters	Inverter 1	Inverter 2	Inverter 3
Resistance R (Ω)	0.8	1.0	0.7
Inductance L (mH)	0.6	0.8	0.5

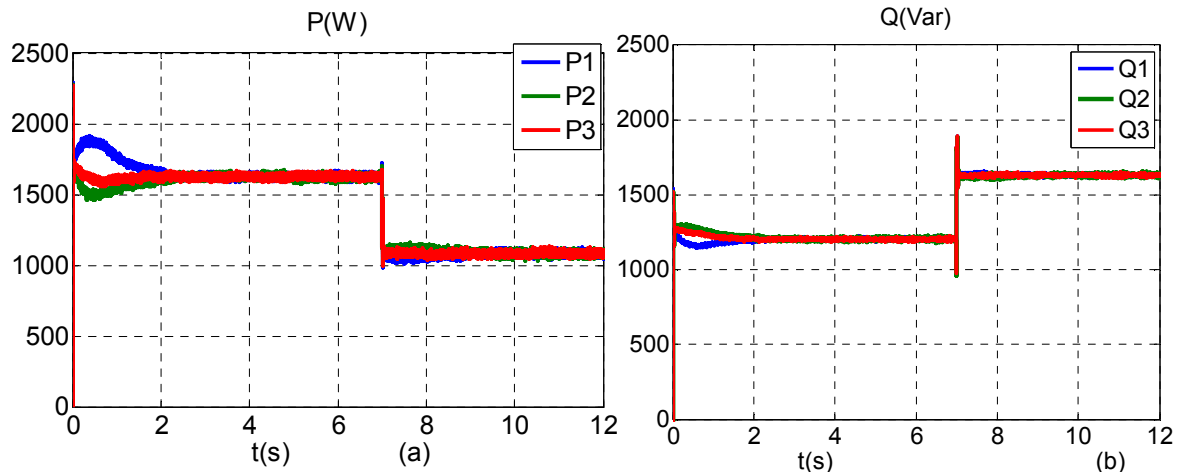


Figure 22. (a) Real power; (b) reactive power.

Figure 22 (a) and Figure 22 (b) can be seen that the proposed control method provides a good power sharing. Figure 22 can shows accurate real and reactive power with a 1:1:1 ratio.

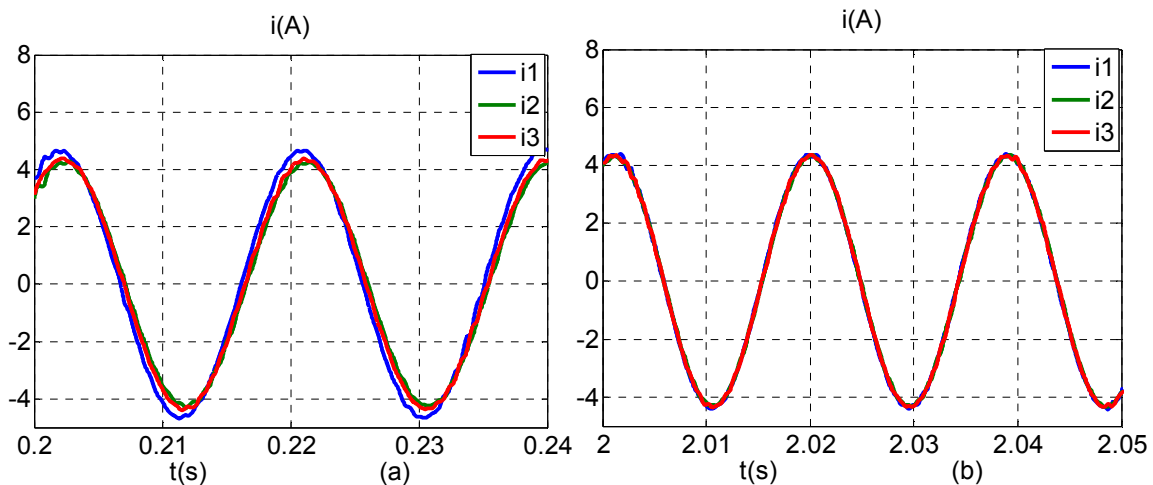


Figure 23. The current output of inverters.

Figure 23 (a) shows response of phase current at output of inverters, we can see that during this time the controller has not reached the set state so there is a mismatch in the power sharing, so that the phase current is mismatch also.

Figure 23 (b) is shown the response of phase currents in satablity the current sharing is not mismatched.

4.5. Simulation for Power Sharing of Two Identical Inverters, the Line Impedances Are Difference, the Communication Is Interrupted

In this case, the line parameters of the two inverters are

4.5.1. Simulation Results with the Proposed Control

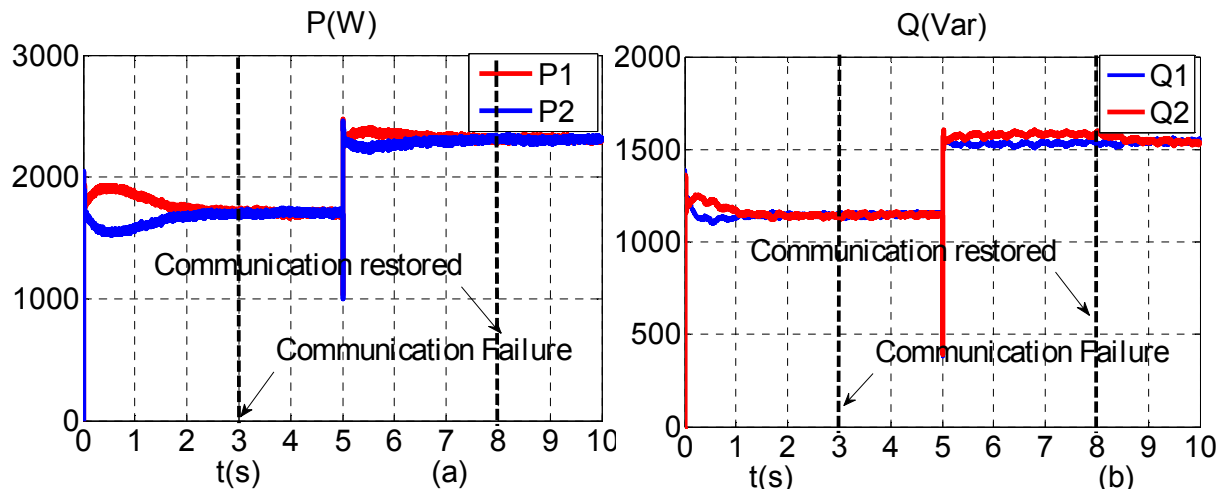


Figure 24. (a) Real power; (b) reactive power.

4.5.2. Simulation Results with the Conventional Droop Control

In order to improve the performance of the reactive power sharing under the effect of the line impedance, some simulation tests have been carried out with the same scenario as in the section 4.5.1. However, the conventional droop control method is applied as shown in (5) and (6). The simulation results are shown in Figure 25.

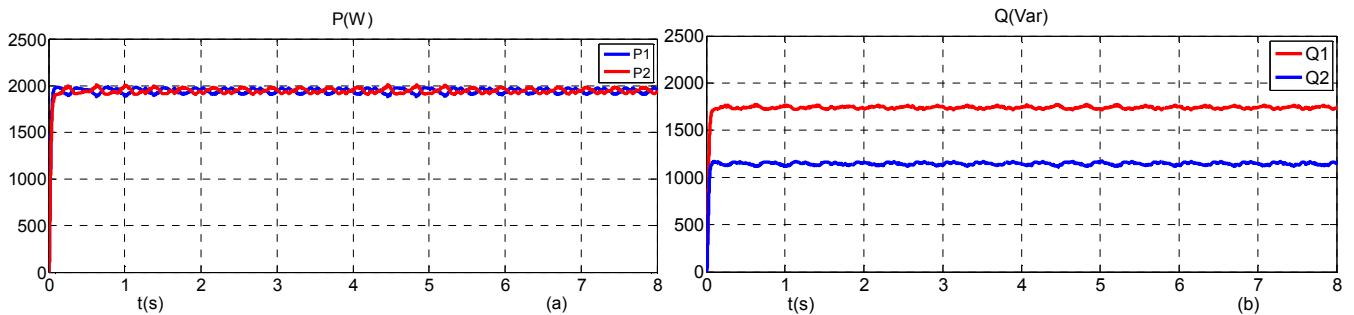


Figure 25. (a) Real power; (b) reactive power.

Figure 24a, Figure 24b show that in the period from 3s to 5s, although communication failure, but the load are not changed so the power sharing has been implemented correctly; in the period from 5s to 8s, the communication failure and the load

are changed so the reactive power sharing hasn't been implemented correctly, but still better than the conventional droop controller in Figure 25b. The communication be restored after the 8s, so the power sharing has been

Table 6. Line parameters of two inverters.

Line parameters	Inverter 1	Inverter 2
Resistance R (Ω)	0.8	1.2
Inductance L (mH)	0.6	1.0

The communication is interrupted at $t=3s$ and the communication is restored at $t=8s$, the load are changed in the period from $t=5s$ to $t=8s$.

implemented correctly.

4.6. Simulation in the Case of the Information Update Delay

Table 7. Line parameters of two inverters.

Line parameters	Inverter 1	Inverter 2
Resistance R (Ω)	0.8	1.2
Inductance L (mH)	0.6	1.0

The line parameters of the two inverters for this simulation are provided in Table 7.

The effect of time delays in communication is investigated by introducing a delay in the signal sent to proposed controller 1, not delay for proposed controller 2. In this case, the proposed controller 2 receives the V_{pcc} reference and starts acting before proposed controller 1. Which has more effect on the transients in comparison to the case when the delays are identical. The introduced time delay is chosen as 0.02s, which is significant given that the reference update period is 200 μ s. Simulation results are illustrated in Figure 26 and Figure 27.

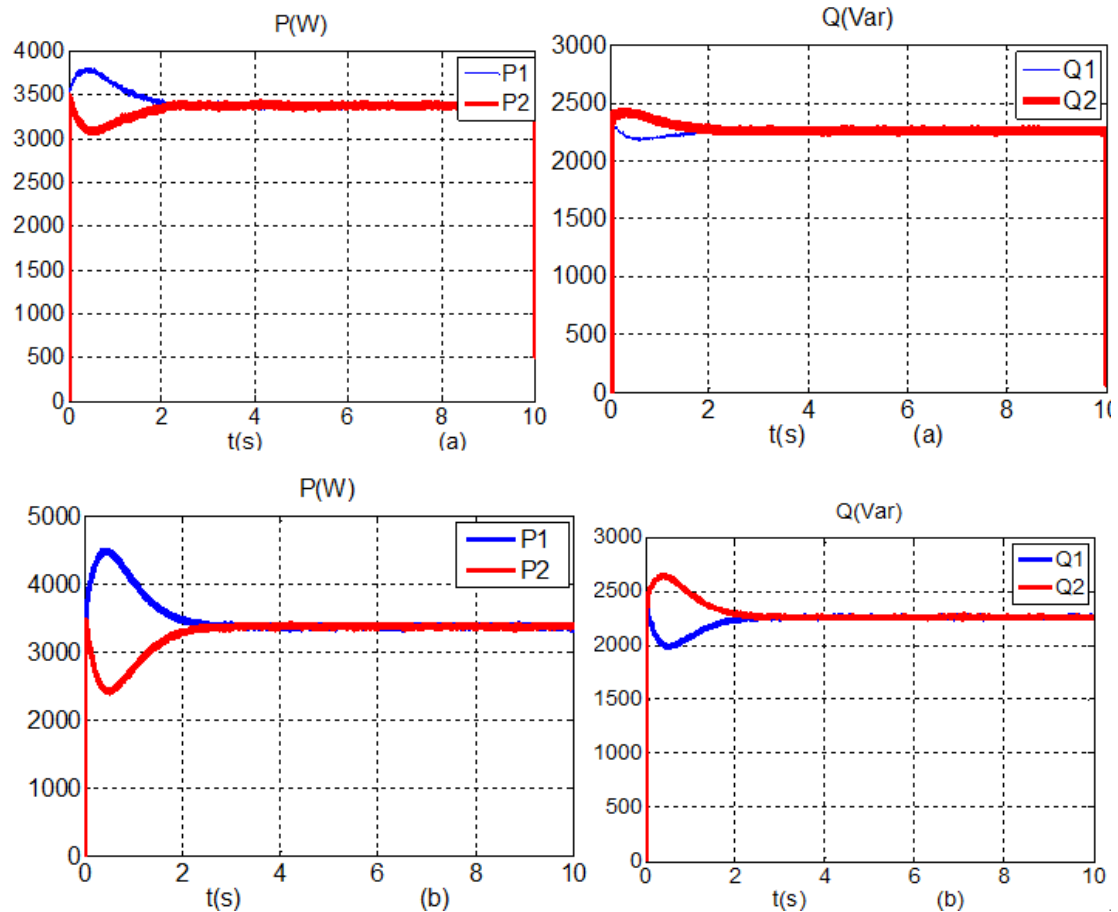
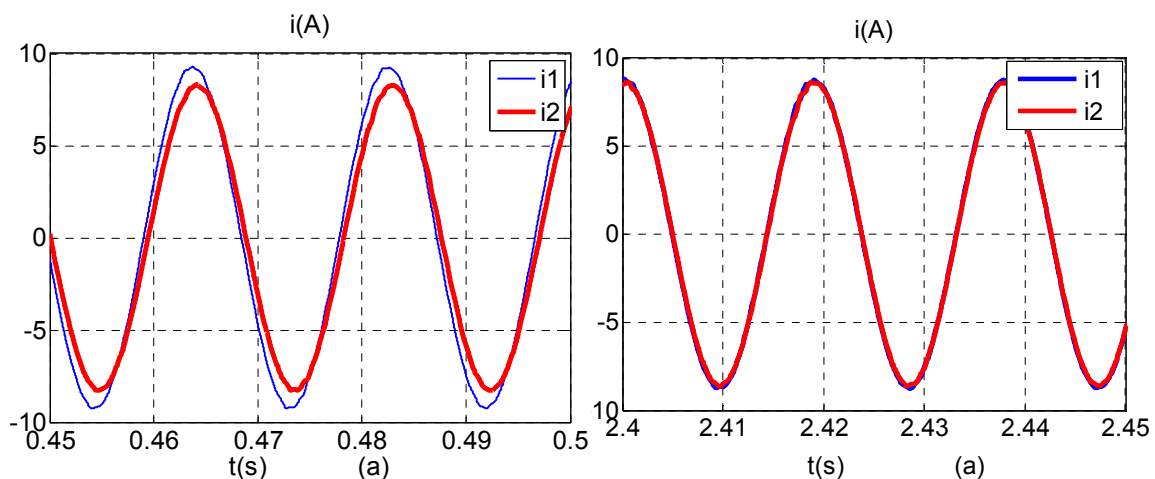


Figure 26. (a) Real power and reactive power when the proposed controller has not been delay; (b) Real power and reactive power when the proposed controller has been delay.



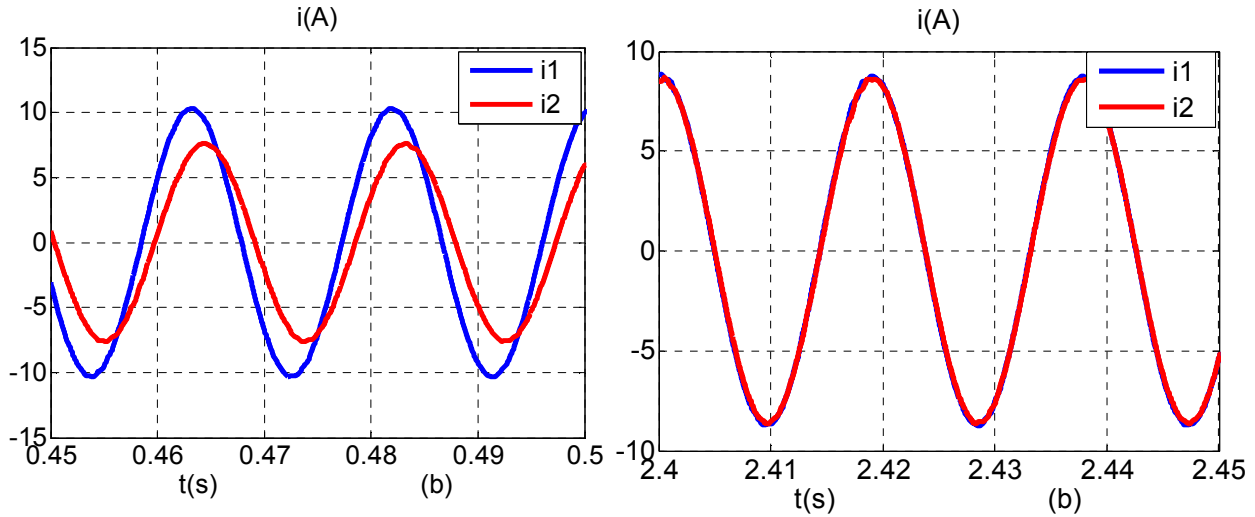


Figure 27. (a) Current output when the proposed controller has not been delay; (b) Current output when the proposed controller has been delay.

The effect of time delays in communication is investigated by introducing a delay in the signal sent to proposed controller 2, not delay for proposed controller 1. The introduced time delay is chosen as 0.1s, a delay occurs at time $t = 5$ s. Simulation results are illustrated in Figure 28.

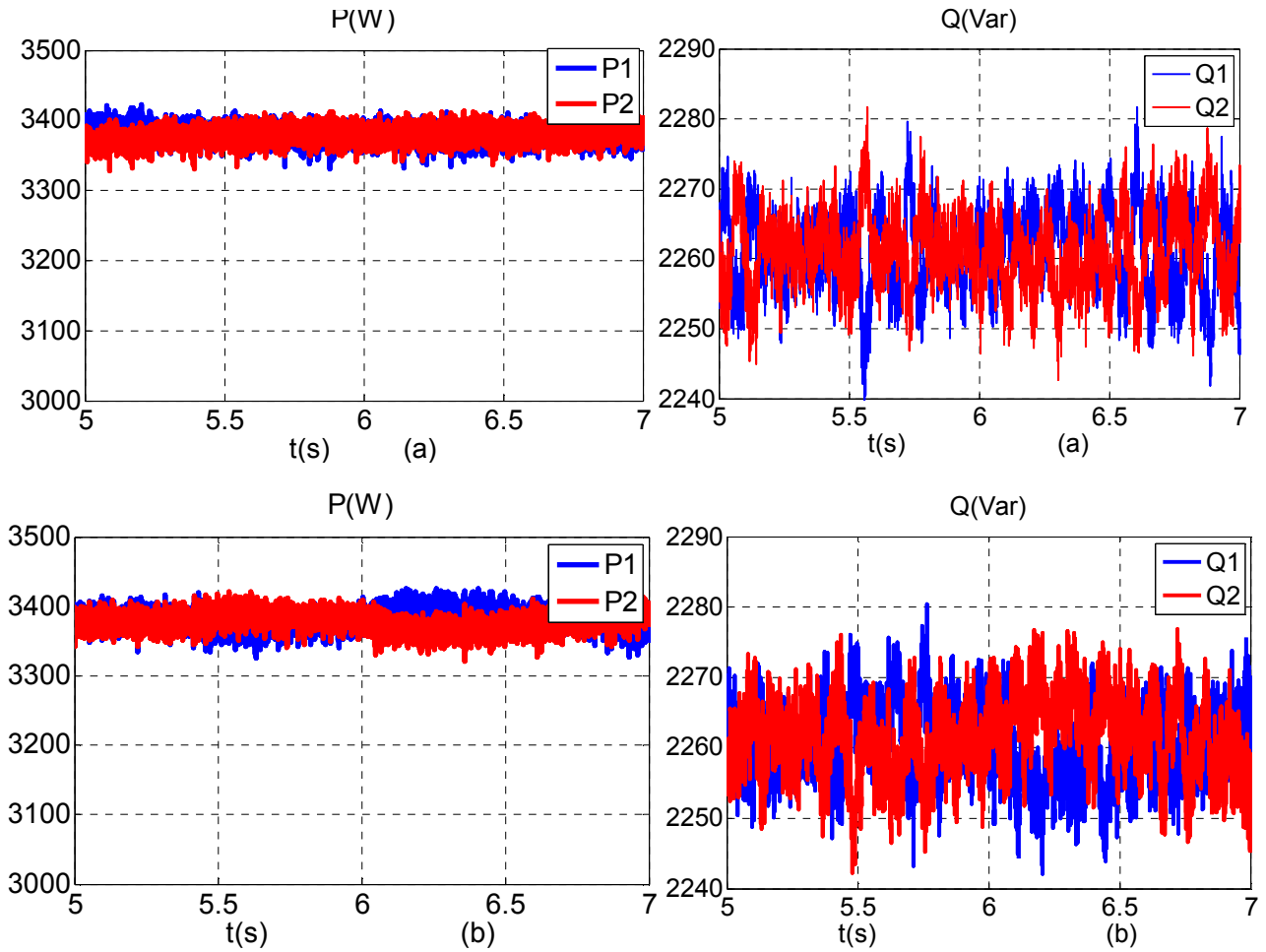


Figure 28. (a) Real power and reactive power when the proposed controller has not been delay; (b) Real power and reactive power when the proposed controller has been delay.

Figure 26, Figure 27 and Figure 28 shown that the time delay has little effect on the system transients. Most importantly, the time delay does not affect the accurate power sharing of the proposed controller. If delays occur in a steady state as Figure 28, it

will not affect on the system transients.

The simulation results by the proposed controller are summarized in Table 8.

Table 8. Reactive power sharing error for selected operating cases.

$P_1: P_2$	Conventional Droop	Proposed controller		
		Communication available	Communication Interrupted	
			The loads don't change	The loads change
	$\Delta Q_{1,2} (\%)$	$\Delta Q_{1,2} (\%)$	$\Delta Q_{1,2} (\%)$	$\Delta Q_{1,2} (\%)$
1:1	24	0.22	0	1.33
2:1	67	0.22	0	3.4

The experimental results by the proposed controller by research [10] are summarized in Table 9.

Table 9. Reactive power sharing error for selected operating cases.

$P_1: P_2$	Conventional Droop	Proposed controller		
		Communication available	Communication Interrupted	
			The loads don't change	The loads change
	$\Delta Q_{1,2} (\%)$	$\Delta Q_{1,2} (\%)$	$\Delta Q_{1,2} (\%)$	$\Delta Q_{1,2} (\%)$
1:1	24	0.43	1.47	2.96
2:1	67	0.43	5.4	8.6

Table 8 and table 9 show that the proposed controller has better results than the study [10]. However, if the communication bus is interrupted, and the load changes, the result of the reactive power sharing will give a significant deviation, but it is still much better than using a traditional controller.

Research [10] has not considered the effect of local loads in Microgrid on the proposed controller.

5. Hardware Implementation Using a DSP

In this paper, a practical model has been developed for testing the proposed method. The developed hardware model consists of three 3-phase inverters, drivers of Semikron, LEM HX 20P and LV-25P are used as voltage and current sensors as shown in Figure 26. The proposed control method has been implemented on a TMS320F28335 DSP controller and the results obtained from the experiment have been captured by a Tektronix TDS2014B oscilloscope and a Fluke 345 PQ clamp

meter. To maintain the load demand, the three inverters have been used with a parallel output connection while RS485 lines are used as a communication network. The experiment has been carried out on three test cases with different ratios for real and reactive powers. The results obtained from the experiment have verified the advantages of the proposed control method through case studies.

5.1. Case Study 1: $P_1: P_2: P_3 = 1:1:3$, and the Load Changes

This case corresponds to the ratio of the active powers being 1:1:3 and load changes with steps within pre-determined limits. The measured active power outputs for the three inverters are shown in Figure 30. The obtained active power outputs for the three inverters increase within the limits as $P_{1min} = 480W$, $P_{2min} = 480W$ and $P_{3min} = 1450W$ $P_{1max} = 750W$, $P_{2max} = 750W$, $P_{3max} = 2250W$. These results have demonstrated the response capability of the system based on the new control strategy when the load continuously changes online with a constant ratio. The active power sharing errors for this case are very small.



Figure 29. Hardware setup for the experiment.

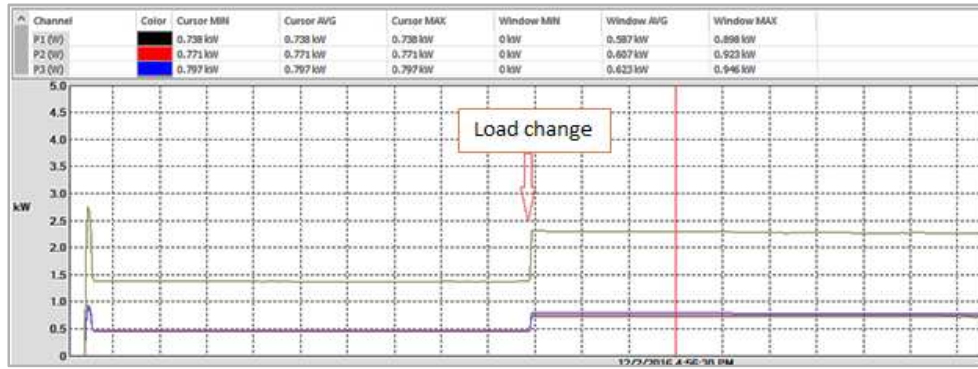
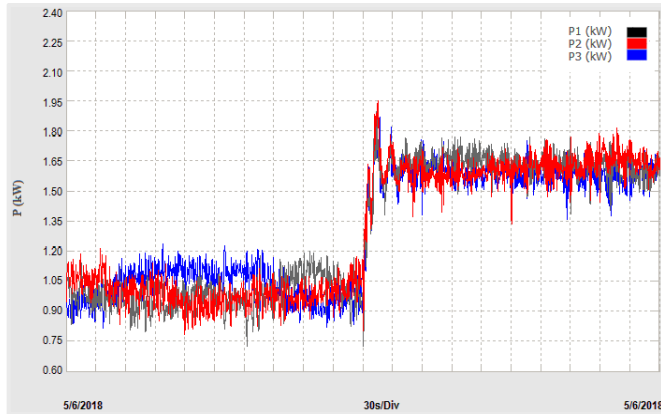


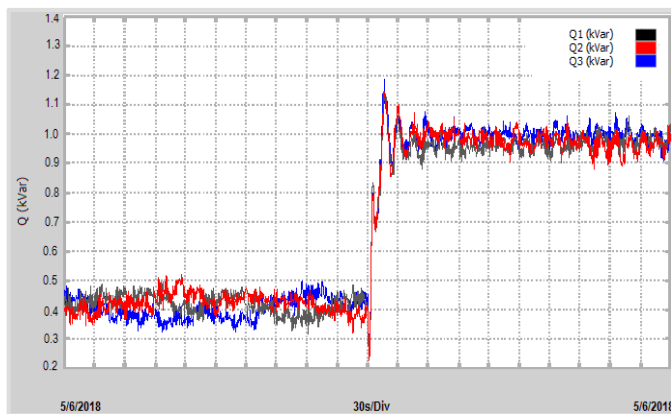
Figure 30. Real power sharing.

5.2. Case Study 3: $P1: P2: P3 = 1:1:1$, $Q1: Q2: Q3 = 1:1:1$, and the Load Changes

Figure 31 shows the active and reactive powers of the three inverters in case of load changes. It can be seen that the ratio of the active and reactive powers is still kept at 1:1:1 when the load increases and decreases.



(a)



(b)

Figure 31. (a) Real power sharing; (b) reactive power sharing.

6. Conclusion

This paper has proposed a new method for an accurate load sharing ratio between the paralleled inverters in islanded Microgrids. In this study, the voltage droop slope is tuned to

compensate for the mismatch in the voltage drops across line impedances by using communication links. The method will ensure in accurate power sharing even if the communication is interrupted. If the load changes while the communication is interrupted, the accuracy of power sharing is reduced but the proposed method is better than the conventional droop control method. In addition, the accuracy of power sharing base on the proposed method is not affected by the time delay in the communication channel and local loads. Simulation results in Matlab/Simulink and hardware experiments have demonstrated the superiority of the proposed strategy in any case with any ratio.

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